



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the **PATENT APPLICATION** of:

Gredone et al.

Application No.: 09/990,060

Confirmation No.: 1472

Filed: November 21, 2001

For: HYBRID PARALLEL/SERIAL BUS
INTERFACE

Group: 2116

Examiner: Nitin C. Patel

Our File: I-2-0201US

Date: October 26, 2005

**FORMAL WRITTEN REPLY IN ACCORDANCE
WITH MPEP SECTION 713.04**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This Reply is being timely filed in response to the Office Action dated October
12, 2005.

O.K.
to
enter pl.
12/7/05